

FIG. 1

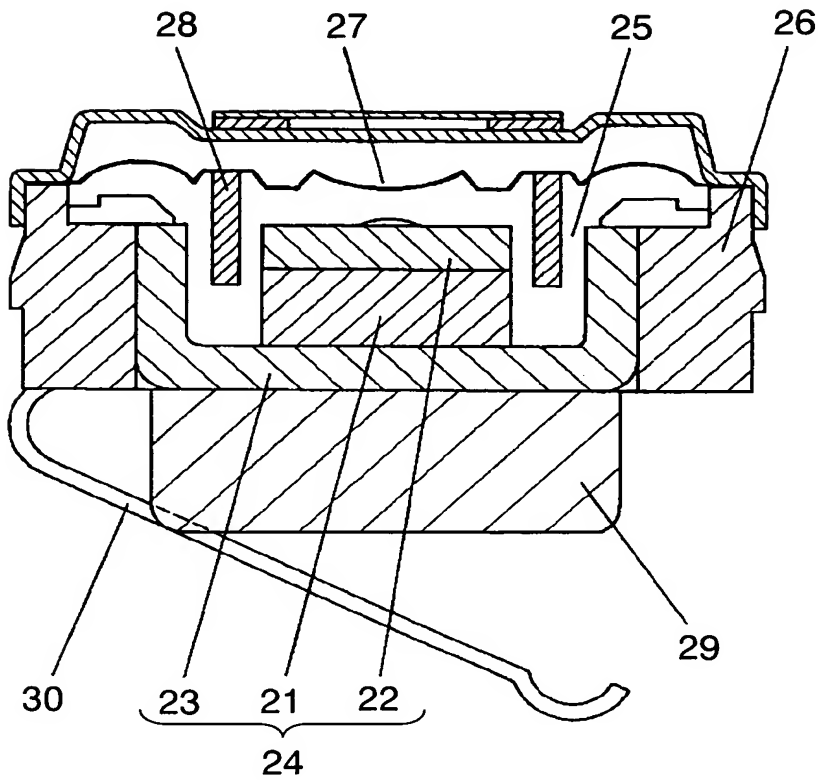
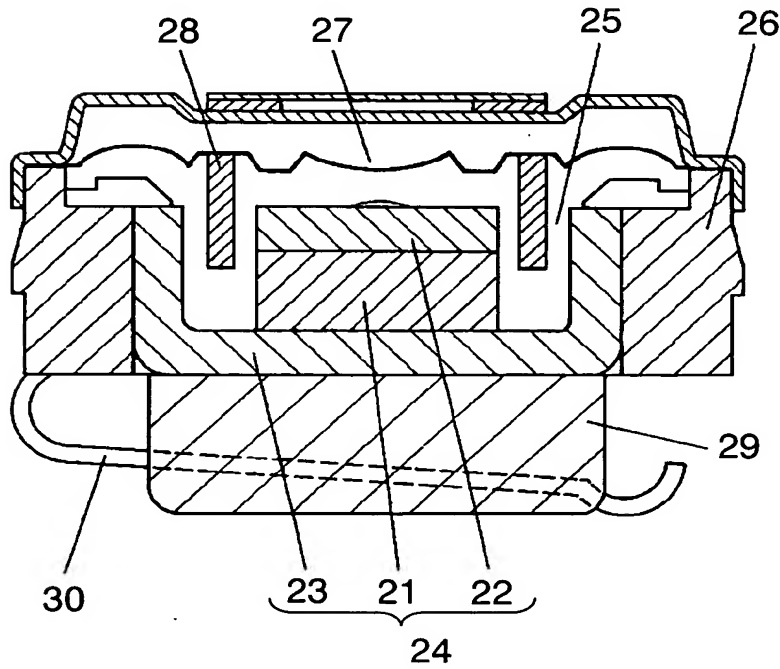


FIG. 2

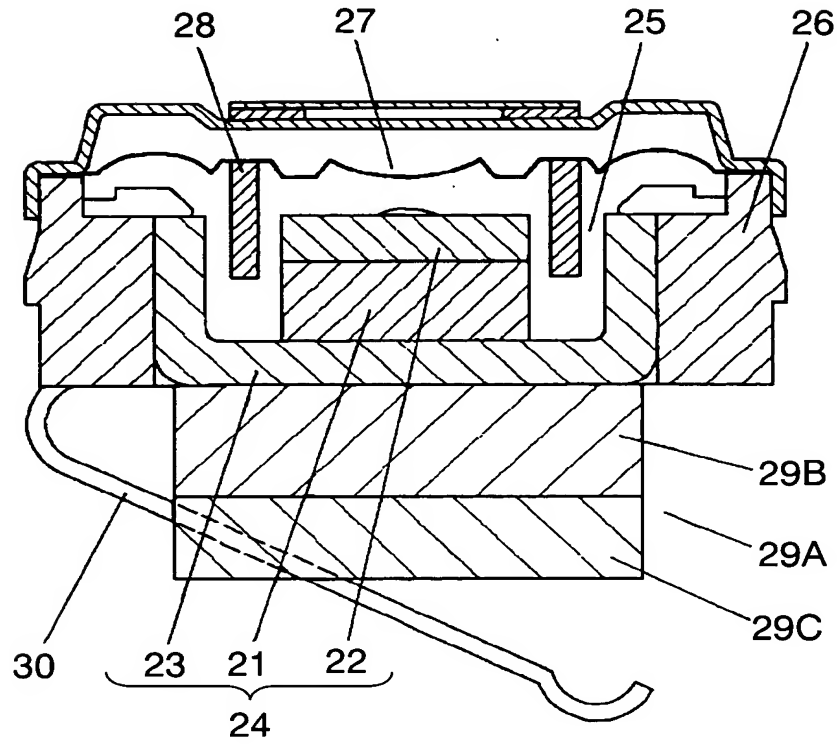


This diagram shows the battery assembly in an exploded perspective view. It includes a battery pack (26) with a rectangular battery cell (29) mounted on top. A cable (23) is connected to the battery pack, and a terminal (30) is shown at the end of the cable. The battery pack (26) has a rectangular shape with a recessed area for the battery cell (29). The cable (23) is connected to the battery pack and has a terminal (30) at the end.

A cross-sectional view of a semiconductor device. The device features a substrate (26) with a top surface (27). A central region (28) is defined by a vertical wall (29). A layer (22A) is deposited on the top surface (27). A layer (23A) is deposited on the side walls (29). A layer (21A) is deposited on the top surface (27). A layer (24A) is deposited on the side walls (29). A layer (25) is deposited on the top surface (27). A layer (30) is deposited on the side walls (29).

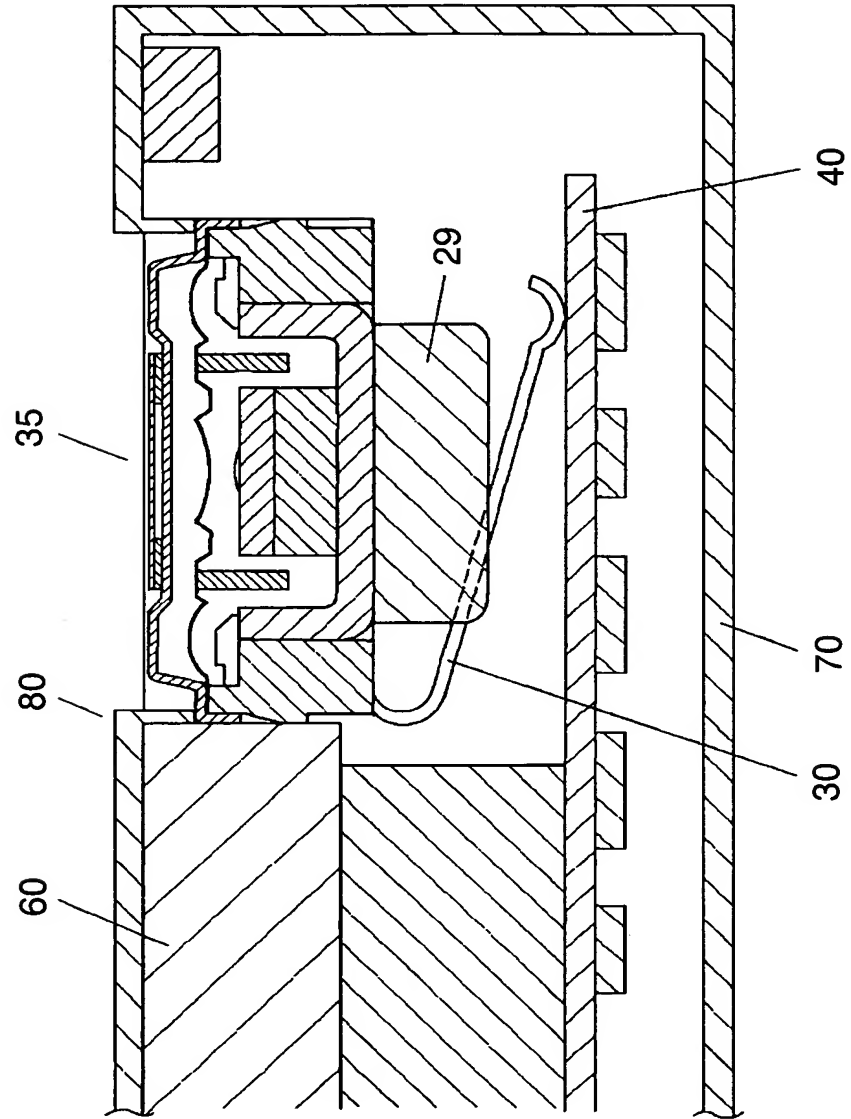
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FIG. 5



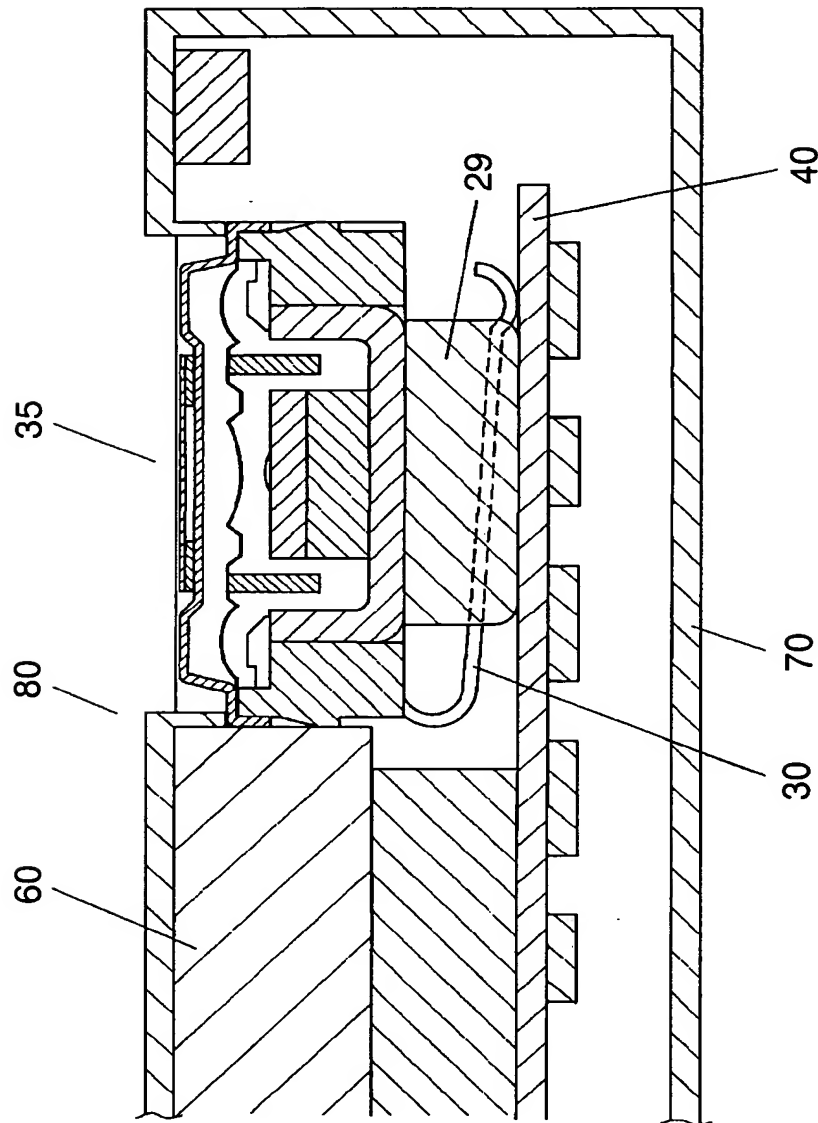
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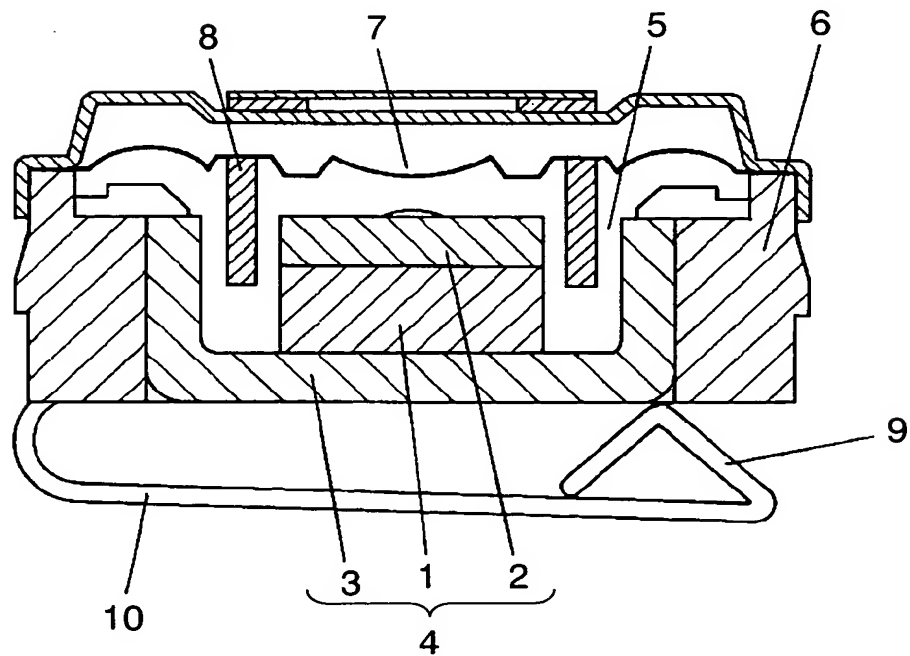
FIG. 6



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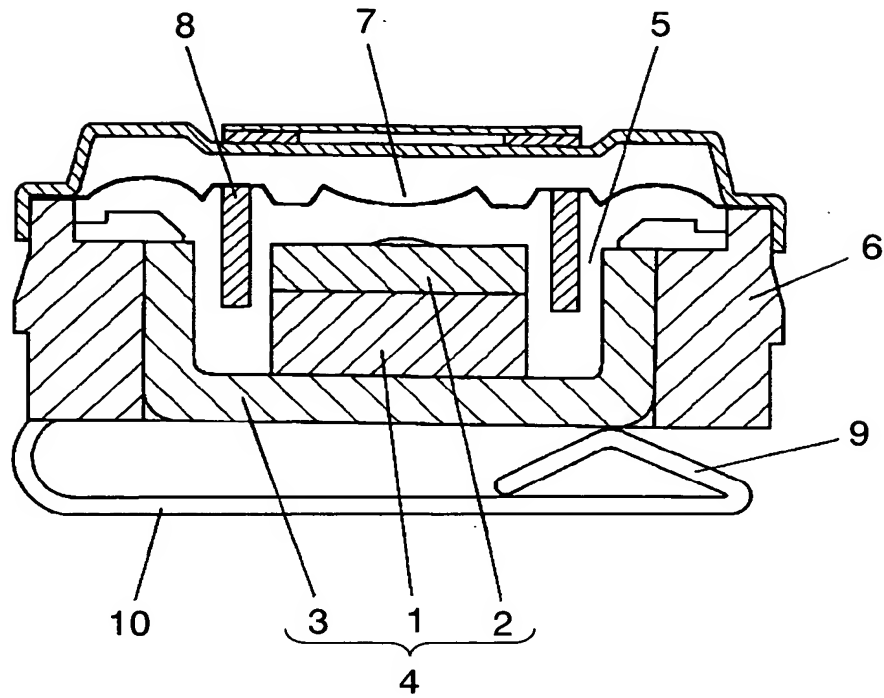
FIG. 7





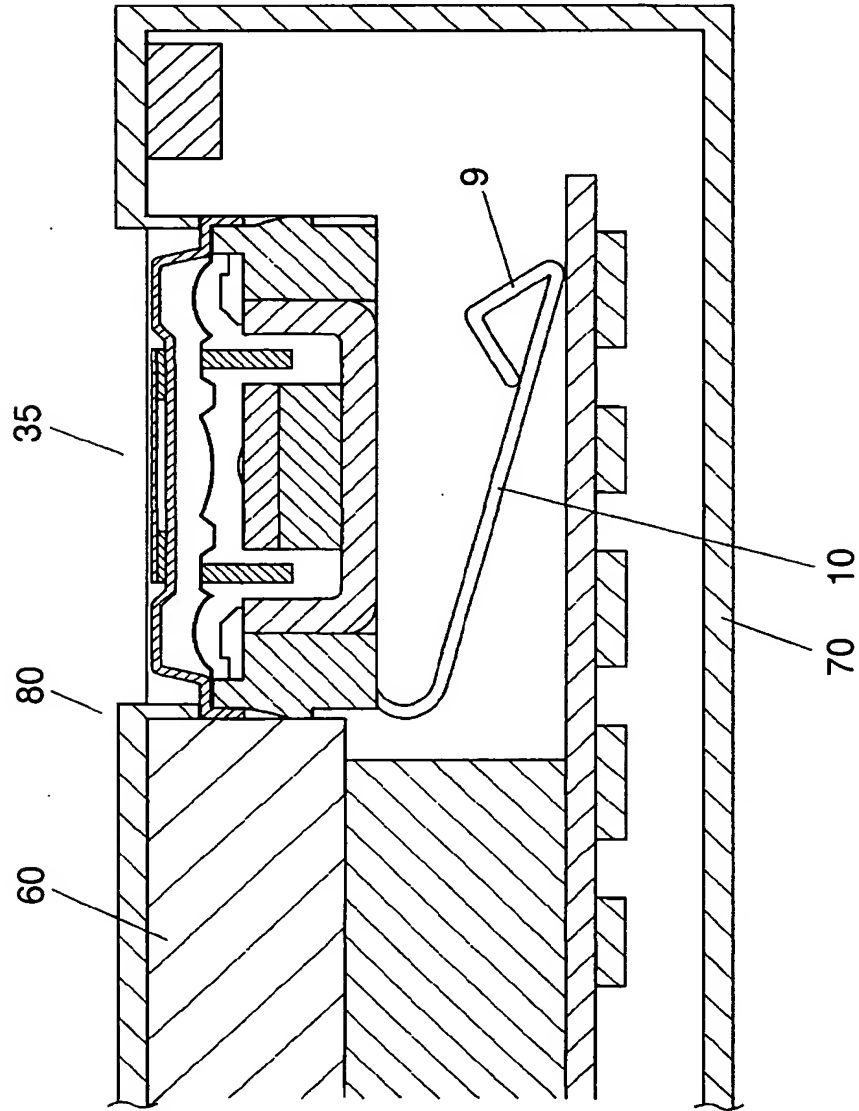
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FIG. 10



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FIG. 11



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FIG. 12

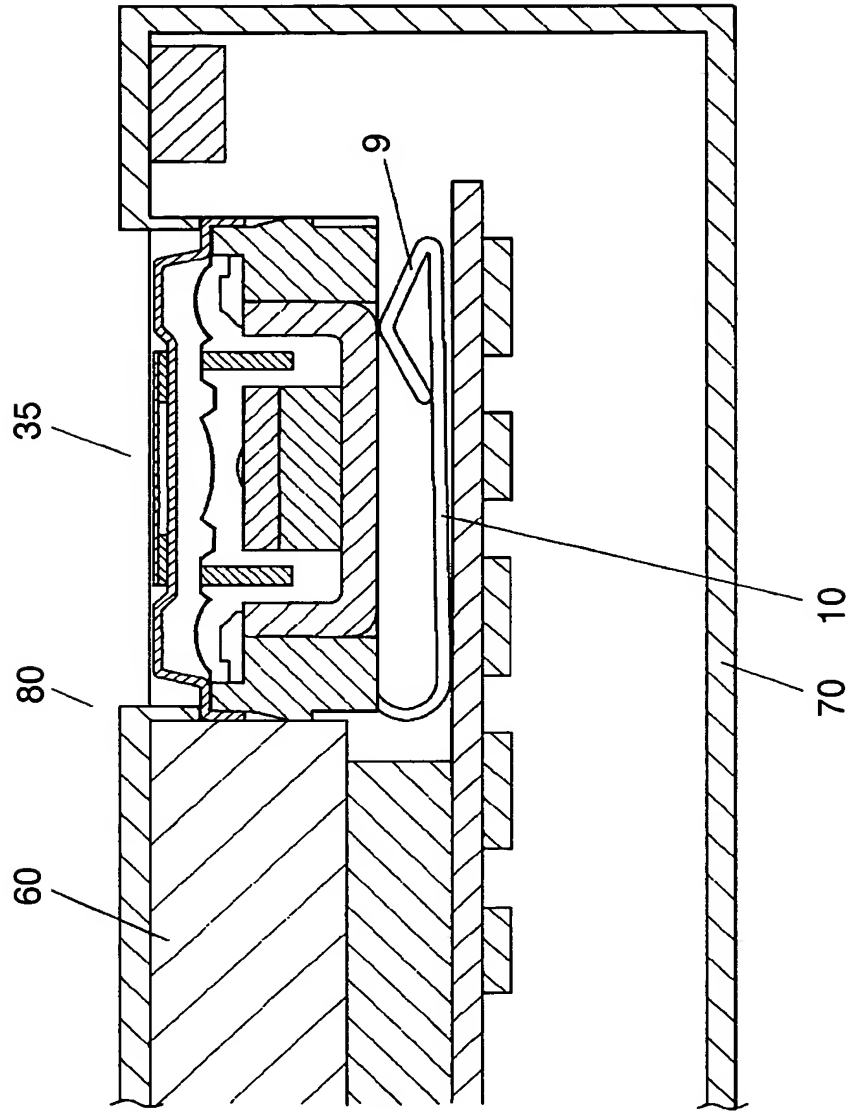


FIG. 13



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Reference marks in the drawings

21, 21A	Magnet
22, 22A	Upper Plate
23	Yoke
23A	Lower Plate
24	Magnetic Circuit
25	Magnetic Gap
26	Frame
27	Diaphragm
28	Voice Coil
29, 29A	Stopper
29B	Elastic Body(Rubber)
29C	Elastic Body(Urethane Foam)
30	Terminal
35	Speaker
40	Electronic Circuit
60	Display Module
70	Outer Case
80	Portable Telephone Unit